

At-a-Glance Codename Decoder

This document is designed to help decipher publicly referenced, forward-looking product and micro architecture codenames discussed in presentations at the AMD Financial Analyst Day on February 2, 2012. Codenames are alphabetically listed and/or indicated in “”. See Product Roadmaps for visual representation of these details.

“Abu Dhabi” CPU (Server)

- “Abu Dhabi” is designed for 2P and 4P enterprise/mainstream server markets.
- This CPU retains the existing G34 socket infrastructure, while utilizing up to 16 next-generation “Piledriver” based cores.
- Planned for introduction: H2 2012

“Brazos 2.0” APU (Essential Desktop and Notebook, Netbook, All-In-One and Small Desktop)

- The “Brazos 2.0” family of APUs will follow “Brazos”, AMD’s fastest ramping platform ever.
- In addition to increased CPU and GPU frequencies, “Brazos 2.0” will offer additional features and functionality as compared to “Brazos”.
- Planned for introduction: H1 2012

“Cape Verde” Graphics

- Cape Verde” is an upcoming discrete graphics product based on the new Graphics Core Next architecture, utilizing the cutting-edge 28nm process node.
- Planned for introduction: Q1 2012

“Delhi” CPU (Server)

- “Delhi” follows “Zurich” for the 1P Web Hosting/Web Serving and Microserver markets.
- This CPU adds the “Piledriver” core enhancements, while maintaining a consistent platform with “Zurich.”
- Expected to be available with four or eight cores.
- Planned for introduction: H2 2012

“Excavator” Core Micro Architecture

- “Excavator” is the evolution of AMD’s “Steamroller” core architecture.

“Hondo” APU (Tablet)

- “Hondo” is AMD’s sub-5W APU designed for tablets. “Hondo” will feature low-power “Bobcat” CPU cores and support DirectX® 11 technology in a BGA or pin-less format.
- Planned for introduction: H2 2012

“Jaguar” Core Micro Architecture

- “Jaguar” is the evolution of AMD’s “Bobcat” core architecture for low-power APUs.

“Kabini” APU (Essential Desktop and Notebook, Netbook, All-In-One and Small Desktop)

- The “Kabini” APU is AMD’s second generation low-power APU and follow-on to “Brazos 2.0.”
- In addition to new “Jaguar” cores, these APUs will be enhanced with new Heterogeneous Systems Architecture (HSA), enabling features for easier programming of accelerated processing capabilities.
- Planned for introduction: 2013

“Kaveri” APU (Notebooks and Desktops)

- “Kaveri” is AMD’s third generation APU for mainstream desktop and notebooks.
- These APUs will include “Steamroller” cores, and new HSA-enabling features for easier programming of accelerated processing capabilities.
- Planned for introduction: 2013

“London” Graphics

- “London” is the internal codename for the AMD Radeon™ HD 7000M series of notebook graphics products.
- AMD Radeon HD 7000M products based on the TeraScale 2 architecture first introduced in December 2011.
- Planned for introduction: Q2 2012

“New Zealand” Graphics

- “New Zealand” is the internal codename for the ultra-enthusiast dual GPU graphics card that will be added to the “Southern Islands” family later this year.
- Based on the new Graphics Core Next architecture, utilizing the 28nm process technology.

“Piledriver” Core Micro Architecture

- “Piledriver” is the next evolution of AMD’s revolutionary “Bulldozer” core architecture.
- The “Trinity” line-up of APUs will be the first introduction of “Piledriver.”

“Pitcairn” Graphics

- “Pitcairn” is the internal codename for an upcoming discrete graphics product based on Graphics Core Next architecture, utilizing the cutting-edge 28 nm process node.
- Planned for introduction: Q1 2012

“Sea Islands” Graphics Architecture

- New GPU Architecture and HSA Features
- Planned for introduction: 2013

“Seoul” CPU (Server)

- The “Seoul” CPU, which is expected to offer up to eight “Piledriver” cores, is created for the 1P and 2P market focused on cost-optimized, energy-efficient platforms.
- This CPU maintains the existing C32 infrastructure.
- Planned for introduction: H2 2012

“Southern Islands” Discrete Graphics

- Internal codename for the entire family of desktop graphics ASICs based on Graphics Core Next architecture and utilizing 28nm process technology.
- “Southern Islands” products include “Tahiti” (AMD Radeon™ HD 7900 series), “Pitcairn,” “Cape Verde” and “New Zealand.”

“Steamroller” Core Micro Architecture

- “Steamroller” is the evolution of AMD’s “Piledriver” core architecture.

“Tahiti” Graphics

- “Tahiti” is the internal codename for the AMD Radeon™ HD 7900 desktop graphics series, which is based on the new Graphics Core Next architecture and utilizes the cutting-edge 28nm process node.
- The AMD Radeon™ HD 7970 graphics card was unveiled on December 22, 2011 and available for purchase on January 9, 2012.
- The AMD Radeon™ HD 7950 launched on January 31, with immediate worldwide availability.

“Temash” APU (Tablet and Fanless Client)

- The “Temash” APU is AMD’s second generation tablet APU and follow-on to “Hondo.”
- In addition to new “Jaguar” cores, these APUs will be enhanced with new Heterogeneous Systems Architecture-enabling features for easier programming of accelerated processing capabilities.
- Planned for introduction: 2013

“Trinity” APU (Traditional Notebooks, Ultrathin Notebooks and Desktops)

- “Trinity” is AMD’s second generation APU and improves the power and performance of AMD’s A-Series APU lineup for mainstream and high-performance notebooks and desktops. “Trinity” will feature next-generation “Piledriver” CPU cores and new, DirectX® 11-capable, second generation AMD Radeon™ HD 7000 series graphics.
- New for 2012, AMD will offer a BGA or pin-less format, low power “Trinity” APU specifically designed for ultrathin notebooks.
- Planned for introduction: Mid-2012

“Vishera” CPU (Desktop)

- The “Vishera” desktop CPU incorporates up to eight “Piledriver” cores, advanced instruction sets and other performance enhancing additions.
- This next-generation CPU will maintain the AM3+ infrastructure.
- Planned for introduction: H2 2012

“Zurich” CPU (Server)

- “Zurich” focuses on 1P Web Hosting/Web Serving and Microserver platforms and leverages the economic efficiencies associated with a volume infrastructure.
- Includes “Bulldozer” core design.
- Planned for introduction: H1 2012

CODENAMES NO LONGER IN USE

“Deccan” (Client Platform)

- Platform codename for essential desktop and notebook, netbook, all-in-one and small desktop systems featuring the “Krishna” or “Wichita” APUs.

“Komodo” CPU (Desktop)

- Six to ten “Piledriver” core desktop CPU. Replaced by “Vishera” (see above) for accelerated time to market, delivers improved performance and next-generation features with existing AM3+ boards.

“Krishna” APU (Essential Desktop and Notebook, Netbook, All-In-One and Small Desktop)

- This APU had two to four “Bobcat” cores for essential notebook, netbook, tablet, all-in-one and small desktop form factors. Planned for 28nm process manufacturing.
- Replaced with “Brazos 2.0” (see above).

“Sepang” CPU (Server)

- Up to 10 next-generation “Bulldozer” cores designed for 1P and 2P market focused on cost-optimized, energy-efficient platforms.
- Replaced by “Seoul” (see above).

“Terramar” CPU (Server)

- Up to 20 next-generation “Bulldozer” cores designed for 2P and 4P enterprise/mainstream server markets.
- Replaced by “Abu Dhabi” (see above).

“Wichita” APU (Essential Desktop and Notebook, Netbook, All-In-One and Small Desktop)

- “Bobcat” dual-core APU for essential notebook, netbook, tablet, all-in-one and small desktop form factors. Planned for 28nm process manufacturing.
- Replaced with “Brazos 2.0” (see above).